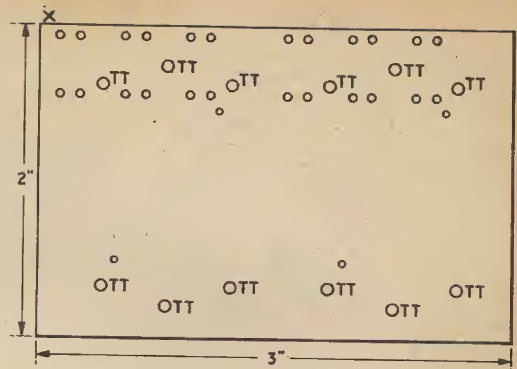
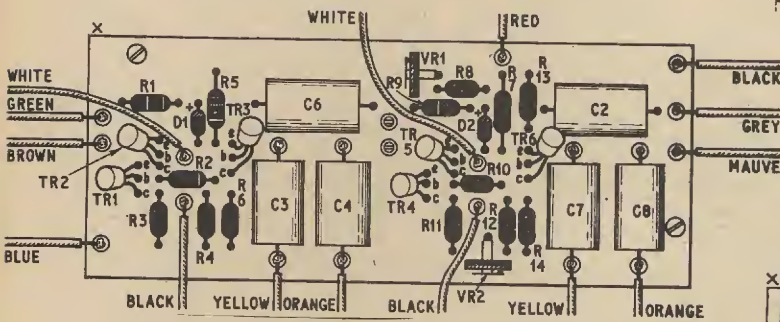


(a)

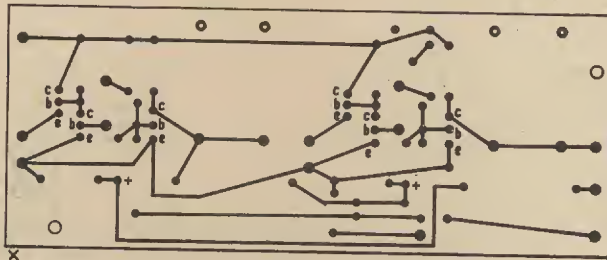


(a)

Fig. 7.2. UNIT "B" integrator switch relay panel. (a) drilling template; (b) component arrangement; (c) underside wiring



(b)

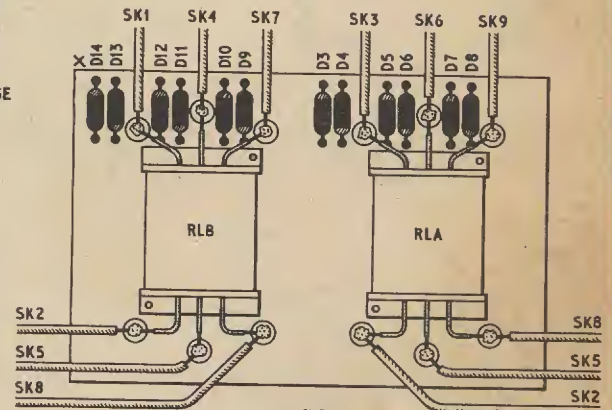


(c)

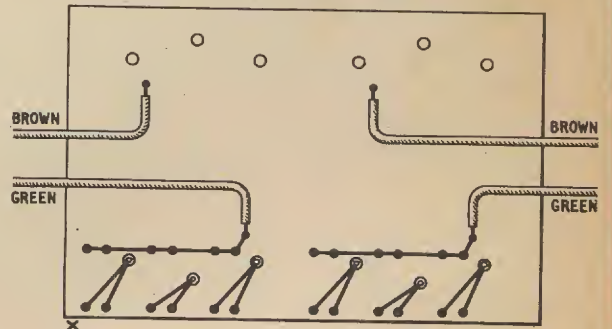
Fig. 7.1. UNIT "B" integrator switch circuit panel, (a) drilling template; (b) component arrangement; (c) underside wiring

Build up the value of C8 timing capacitor by adding more capacitors in parallel, until the +9V output is obtained when S7 is pressed.

To check the "fast" end of VR19 scale, set VS1 for -9V and rotate VR19 fully anti-clockwise. Adjust VR2 on the integrator switch panel to obtain the desired amplifier output of +9V for a compute interval of 0.1s.



(b)



(c)

(c)

Section of UNIT "B" panel (viewed from rear) showing integrator switch relay assembly

