

Fig. 9.5. Internal layout and wiring of UNIT "D" multiplier

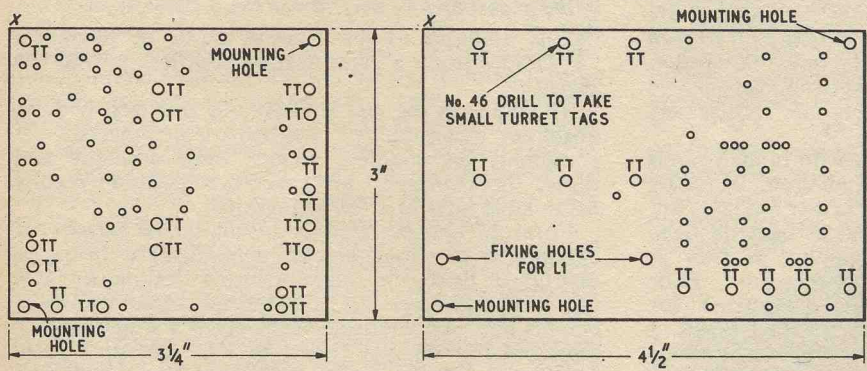


Fig. 9.6 (far left). Top and underside views of bistable relay panel

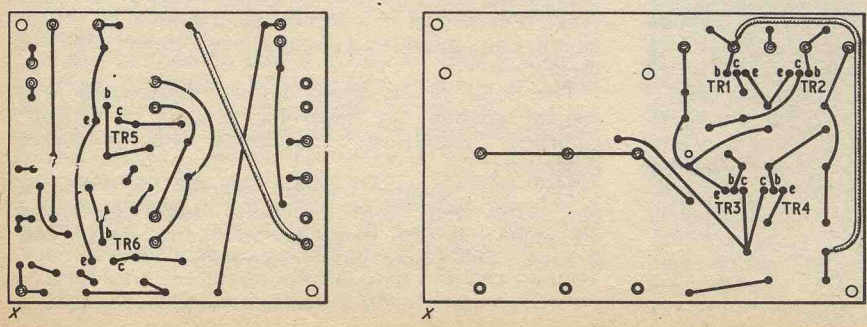


Fig. 9.7 (left). Top and underside views of product amplifier circuit panel