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A FAST FOURIER TRANSFORM SUBROUTINE
FOR ILLIAC IV

by

James E. Stevens, Jr.

October 15, 1971

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ABSTRACT

This report is a description of a Fast Fourier Transform (FFT) subroutine written in assembly language for the ILLIAC IV computer. The subroutine uses the Cooley-Tukey algorithm for performing discrete Fourier transforms. The parallel nature of the Cooley-Tukey method lends itself very well to a highly parallel machine like ILLIAC IV. Timing simulation results have shown that this program will perform Fourier transforms faster than they are being done on any existing computer system.

TABLE OF CONTENTS

Introduction	1
Subroutine Specifications	2
Implementation of the Cooley-Tukey Procedure	2
Scrambling	8
Future Considerations	15
ILLIAC IV Fast Fourier Transform Users Manual	17
APPENDIX A: Standard Subroutine Linking	19
APPENDIX B: Demonstration Run	20
APPENDIX C: Timing Summary	28
APPENDIX D: Subroutine Listing	29
References	63

Introduction

The Fourier transform is one of the most important mathematical aids to signal processing. The basic property of the Fourier transform is its ability to distinguish waves of different frequencies which have been additively combined. The result of a Fourier transform on a complex function of time is a frequency spectrum of that function. This resulting frequency spectrum is also a complex function. To facilitate processing on digital machines, a method for transforming discrete functions of time into discrete functions of frequency has been derived. The time function must consist of evenly spaced samples of some interval, T . A function of N samples could be represented as $f(nT)$ for $0 \leq n \leq N - 1$. The spectrum of the function would also consist of discrete values evenly spaced in frequency. If the spectrum were composed of N points, the interval of frequency, Ω , would be $2\pi/NT$. The spectrum $F(k\Omega)$, $0 \leq k \leq N - 1$ would be given by

$$F(k\Omega) = \sum_{n=0}^{N-1} f(nT)e^{-jnTk\Omega} \quad (1)$$

This equation defines the discrete Fourier transform (DFT) of a sequence of N samples. It follows directly that the DFT is linear since

$$\text{DFT}\{f(nT) + g(nT)\} = \text{DFT}\{f(nT)\} + \text{DFT}\{g(nT)\} \quad (2)$$

and

$$\text{DFT}\{c(f(nT))\} = c[\text{DFT}\{f(nT)\}] \quad (3)$$

This follows quickly from the definition.

There exists an inverse DFT, a transform which maps a discrete Fourier transform back into the sequence from which it was composed. It is given by

$$f(nT) = \frac{1}{N} \sum_{k=0}^{N-1} F(k\Omega)e^{jnTk\Omega} \quad (4)$$

which differs from the DFT equation 1, only by a scale factor and by the sign of the exponential. The same procedure used for the DFT can thus be applied to a frequency spectrum to perform the inverse Fourier Transform by simply dividing the result by N .

The above method for computing DFT requires N^2 complex operations to be performed in order to transform a function with N samples. The method of Cooley and Tukey [1] eliminates redundant steps in that method resulting in a procedure requiring only $N \log_2 N$ complex operations. This method is highly parallel and therefore very well suited for use on ILLIAC IV [2].

Subroutine Specifications

The ILLIAC IV fast Fourier subroutine accepts discrete input functions composed of N complex samples where N is an even power of two between 8 and 4096, inclusive. The output of the subroutine is a discrete frequency spectrum composed of N complex samples as defined by equation (1). The output is stored over the input in ascending sequence in a contiguous block of memory. N words of storage are used, where each word contains the real (outer) and imaginary (inner) portions of one sample in the 32-bit format. Input to the subroutine must also be stored in this manner; therefore, all values have 24-bit significance and are represented in floating point. Several functions can be input to the subroutine at one time as long as they are stored contiguously and provided that they all are the same length. If the transform of a function of M samples is desired where M is not a power of two, $N-M$ zero samples should be added to the function where N is the smallest power of two greater than M . The result can be input to the subroutine as a function of N samples. The output will be a frequency spectrum equivalent to the transform of the original function of M samples with a smaller frequency spacing.

Implementation of the Cooley-Tukey Procedure

The method of Cooley and Tukey [1] is illustrated in Figure 1. Implementation of this method on ILLIAC IV has been discussed by G.M. Ackins [3]. Ackins' methods have been refined and generalized for use in this subroutine. The following description gives the details of the final implementation.

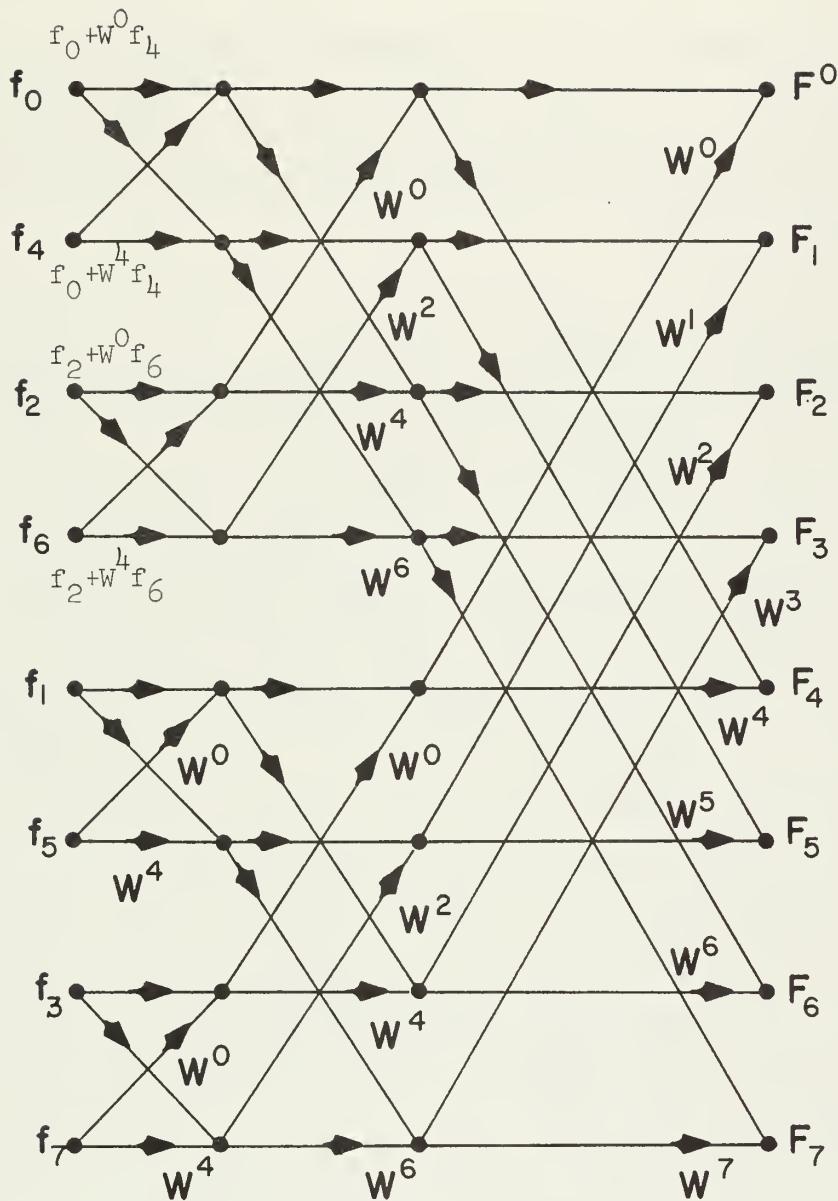


Figure 1

In Figure 1, f_n represents $f(nT)$ and F_n represents $F(n\Omega)$. Also W is used to replace the constant $e^{-j\frac{2\pi}{N}}$. At each node in the diagram, a complex multiply involving the lower of the two inputs to the node and the constant, W , raised to the appropriate power is performed. The result is added to the upper of the two inputs giving an answer which is used as an

input to the next iteration until the transform is completed. At each stage in the process where an iteration is complete, the partial results are stored into the initial locations so that the data is transformed in place. Figure 2 illustrates how this transform would be accomplished in parallel on an eight PE ILLIAC IV.

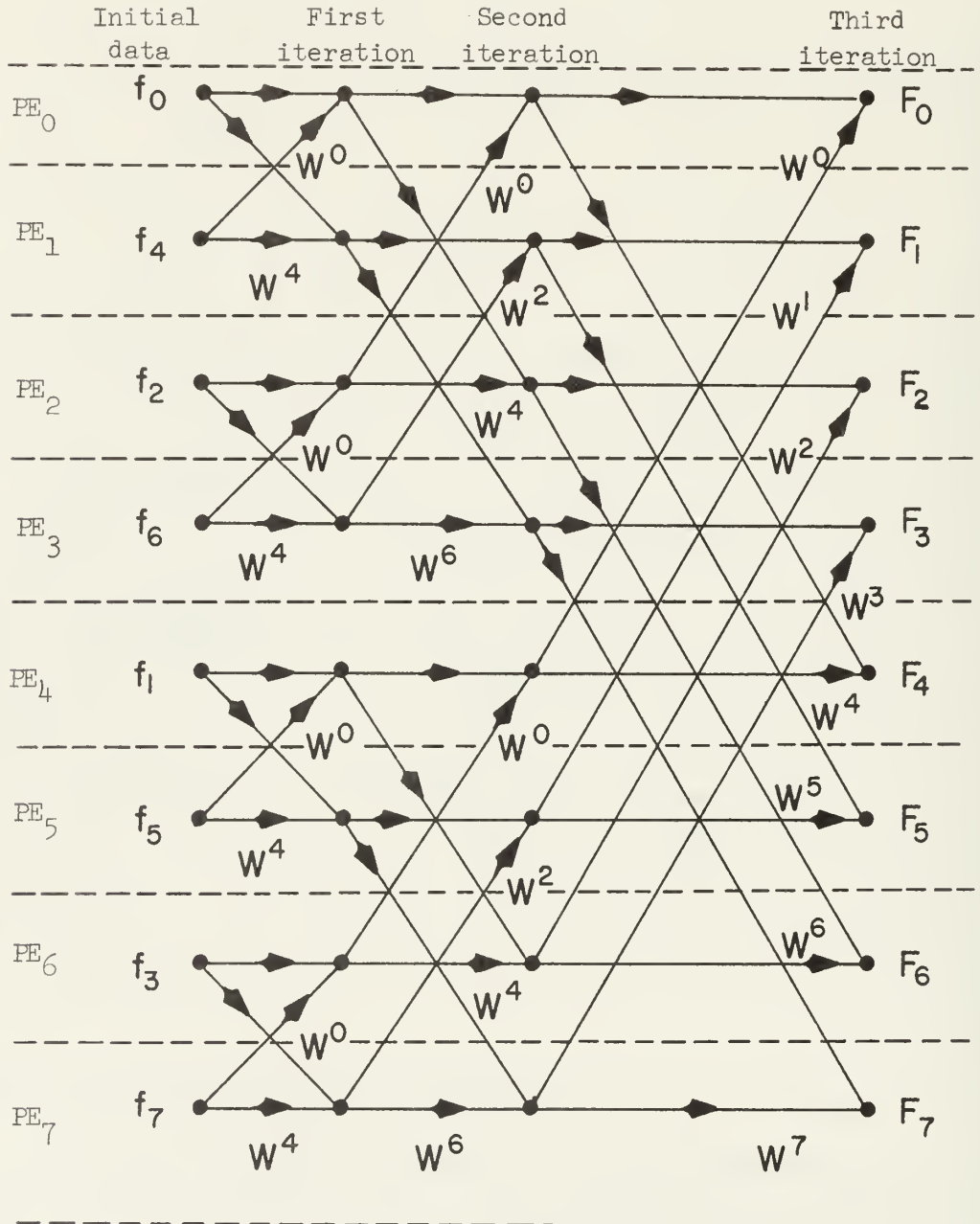


Figure 2

In Figure 2, the arrows represent routing of data between PE's while the nodes represent the operations which take place within the PE's as given for Figure 1. $f(t)$, and all of the necessary powers of W are stored in PE memory for use in the calculation. From Figures 1 and 2, it is seen that the input data has been "shuffled" so that the output appears in ascending sequence. This shuffling also causes the routing (arrow) pattern to be very regular. The algorithm for ordering input data is explained in a later section of this report which describes the routine called Scramble which performs the shuffling in ILLIAC IV.

The method described above and illustrated in Figure 2 can be generalized to transform much larger functions. Careful examination of Figure 1 reveals that after two iterations, two separate four point transforms have been completed. A four point transform is depicted in Figure 3. It should be noted that the value of W depends on the size of the function being transformed.

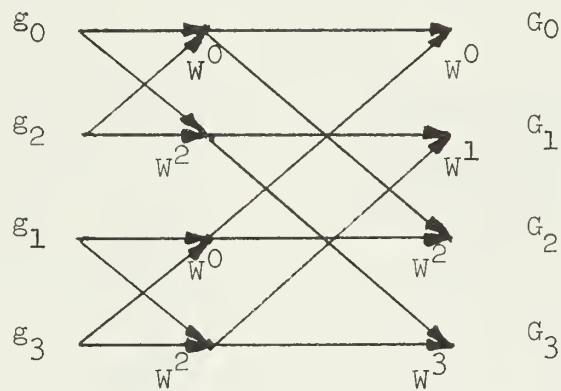


Figure 3

The eight point transform can now be represented in terms of the four point transform as depicted in Figure 4.

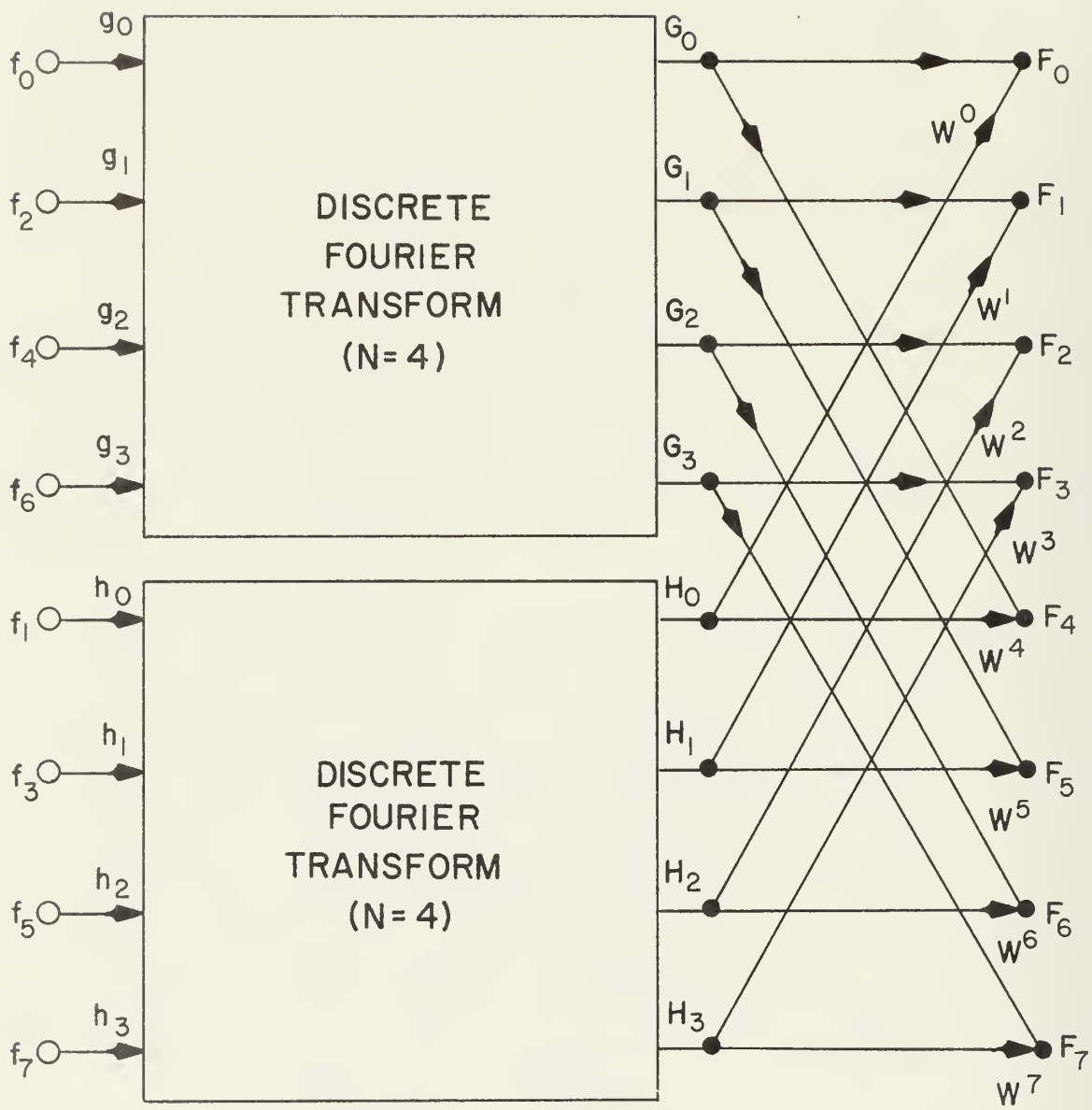


Figure 4

The equality of these two representations (Figure 1 and Figure 4) is important to the structure of the subroutine. In an eight point transform, $W = e^{-j\frac{2}{8}}$, so that $W^4 = e^{-j}$. In a four point transform, $W = e^{-j\frac{2}{4}}$, so that $W^2 = e^{-j}$. Thus, an eight point transform can be accomplished by performing two four point transforms and iterating once more. In the general case any N point transform, where N is a power of two, can be accomplished by performing two N/2 point transforms and combining those results with one final iteration. One consequence of this is that the first step in performing any size transform is to perform an eight point transform on each group of eight points. The entire subroutine is built up from that basis.

The other consequence of this partitioning of the procedure is that each stage is completely independent. When a function composed of 128 data samples is to be transformed on ILLIAC IV, it is simply stored in two rows of PE memory. One row of PE memory consists of one word in each PE. The first 64 samples are stored in one row (after proper scrambling) and the second half are stored in the other row. This is referred to as "folding" 128 words of data into two 64 word rows of memory. A 64 point transform can now easily be performed on the first half of the data since this operation is independent of the other half. This 64 point transform is a simple extension of the eight point transform of Figure 2, using 64 PE's. Another independent 64 point transform is then performed on the second half of the data.

The results of these two 64 point transforms can be combined in one simple iteration giving the final 128 point transform. Since the input data was folded in PE memory, no routing is required to align the two halves for this final iteration simplifying it even further. This can be illustrated by observing that the final iteration of each transform requires a route of N/2 positions. For a 128 point transform this would be a route of 64 which is equal to a route of 0 in a single quadrant ILLIAC IV. Furthermore, every transform larger than 64 requires a final route which is a multiple of 64 which means no routing is actually required. Since there is a smaller proportion of time spent in routing, the largest transform is actually the most efficient in its computation.

The final generalization is that each larger transform above 64 is simply accomplished by folding the function and treating the halves independently until the final iteration. The size of the function will determine how many eight point transforms are done as the first step. Also the number of functions to be transformed at a time can be taken into account through proper loop control. For example, two 2048 point transforms could quite easily be done at the same time. They would each be scrambled separately and then the transform would proceed as if the two functions composed a single 4096 point function--only, the final iteration would not be performed.

Scrambling

The shuffling of data in the processing elements before performing the transform allows the computation to be done in place, that is, by writing all intermediate results over the original data. This process will be referred to as prescrambling and when it is applied the transform generates results which are sequentially ordered across PE's. The original function is composed of discrete samples at even spaced time intervals. They are assigned indices 0 through N in order. Each index can be represented by a binary number with $\log_2 N$ digits, $K_0 K_1 \dots K_n$, where $n = \log_2 N - 1$. The scrambling process requires that each sample having index $K_0 K_1 \dots K_n$ be interchanged with the sample having index $K_n K_{n-1} \dots K_0$. Thus by a simple "bit reversal" of each binary index the destination of each sample in the scrambling process is determined. Note that in Figure 1 a three digit bit reversal on the sequence 0,1,2,3,4,5,6,7 yields the proper sequence for input to the transform procedure, i.e., 0,4,2,6,1,5,3,7. Consider the following example to see how the scrambling is accomplished within ILLIAC IV. The input data is represented by indices only.

	PE ₀														PE ₁₅	
Initial function	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Bit reversed indices	0	8	4	12	2	10	6	14	1	9	5	13	3	11	7	15
1st iteration	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Route (7)																
2nd iteration	0	8	2	10	4	12	6	14	1	9	3	11	5	13	7	15
Route (2)																
Properly ordered	0	8	4	2	2	10	6	14	1	9	5	13	3	11	7	15

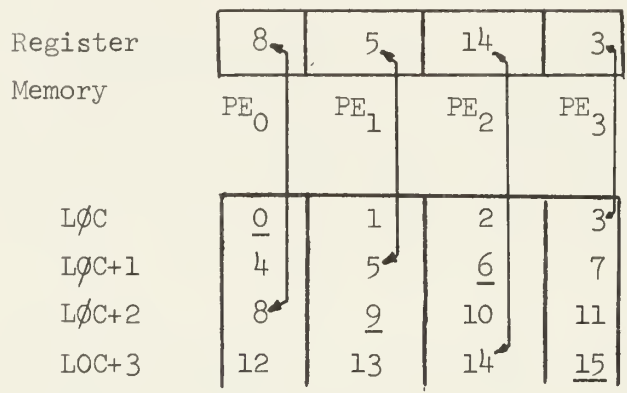
This process can be generalized for scrambling N points in the following manner. The odd numbered PE's less than $N/2$ send data to the corresponding even numbered PE's greater than $N/2$ and receive data back from them. The next iteration is the same except the first $N/2$ PE's and the second $N/2$ PE's perform it independently and handle the data by pairs. Note that only two routes are required for each iteration. By this method ILLIAC IV can scramble a 64 point data set in three iterations requiring only 6 routes. For larger data sets the data must be folded in storage and a different scheme is used. The following example shows how a sixteen point data set would be scrambled by a 4 PE ILLIAC IV.

Proper reordering
determined by bit reversal

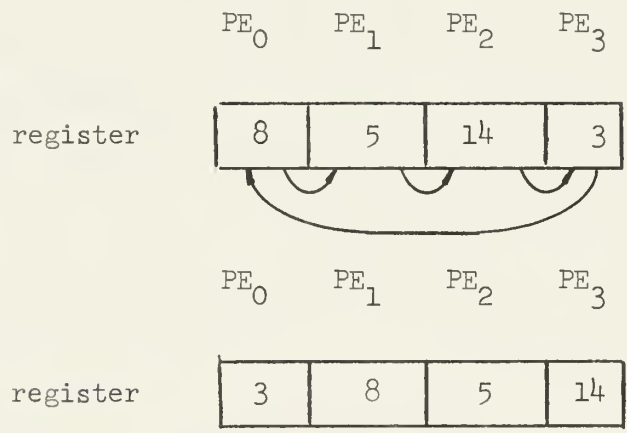
	PE ₀	PE ₁	PE ₂	PE ₃
LOC	0	1	2	3
LOC+1	4	5	6	7
LOC+2	8	9	10	11
LOC+3	12	13	14	15

	PE ₀	PE ₁	PE ₂	PE ₃
	<u>0</u>	8	4	12
	2	10	<u>6</u>	14
	1	<u>9</u>	5	13
	3	11	7	<u>15</u>

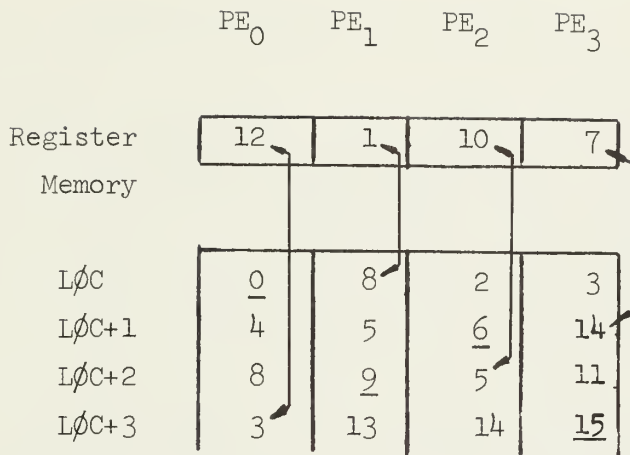
The underlined indices denote the positions that are unaffected by scrambling since data in these positions does not need to be moved. The location of these stationary positions within each PE is given by performing a bit reverse operation on the PE numbers. Each PE now loads a register with data from the location corresponding to the stationary location in the next higher numbered PE.



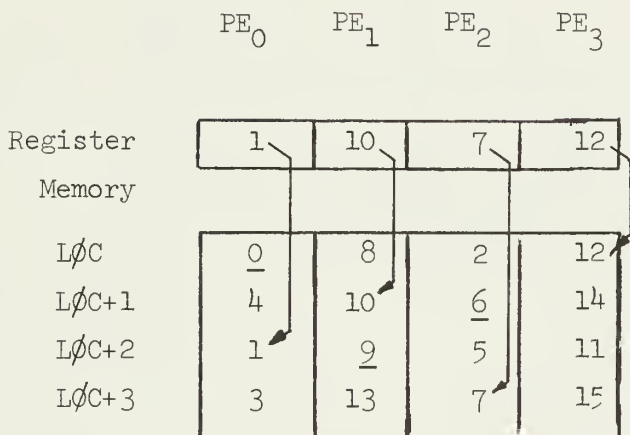
Each PE routes the data in its register to the next higher numbered PE. The rightmost PE routes back to the first PE.



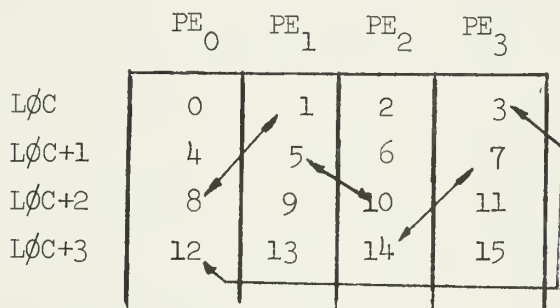
Now the data in each register is exchanged with the data from the location in the PE corresponding to the stationary location in the next lower numbered PE.



The data in the registers is now routed back one PE and stored where the first data came from.



The transfers accomplished can be illustrated as shown below:



The transfers to complete the scrambling would be:

	PE ₀	PE ₁	PE ₂	PE ₃
LØC	0	8	2	12
LØC+1	4	10	6	14
LØC+2	1	9	5	11
LØC+3	3	13	7	15

This final transfer is accomplished by only one route since each data point moves exactly two PE's away (end around). Once again all PE's are active. The following diagram shows how this final step is accomplished:

	PE ₀	PE ₁	PE ₂	PE ₃
LØC	0	8	2	12
LØC+1	4	10	6	14
LØC+2	1	9	5	11
LØC+3	3	13	7	15

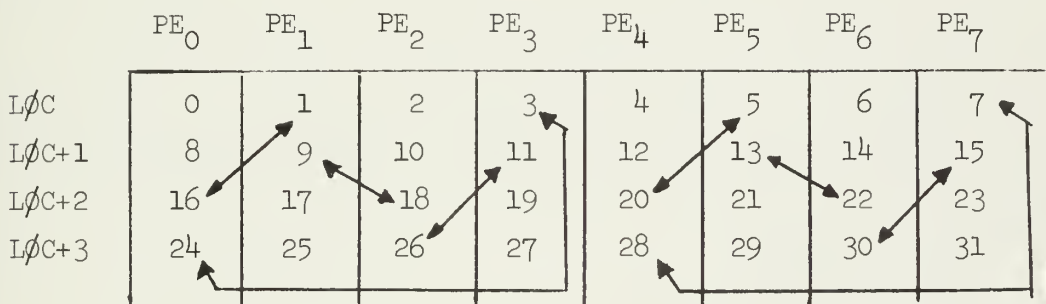
Thus, after two iterations, the data has been properly ordered for input to the transform routine.

	PE ₀	PE ₁	PE ₂	PE ₃
LØC	0	8	4	12
LØC+1	2	10	6	14
LØC+2	1	9	5	13
LØC+3	3	11	7	15

In the operations described above, each PE is always performing some tasks and the data is never moved to an intermediate memory location so that the scrambling is done in the minimum number of steps and with 100% efficiency in the use of PE's. It can be seen that neither of these statements is true about the scheme described at the beginning of this section or the scheme described by Ackins [3]. Unfortunately this algorithm works only on data sets which have N^2 points, where N is the number of PE's in the array.

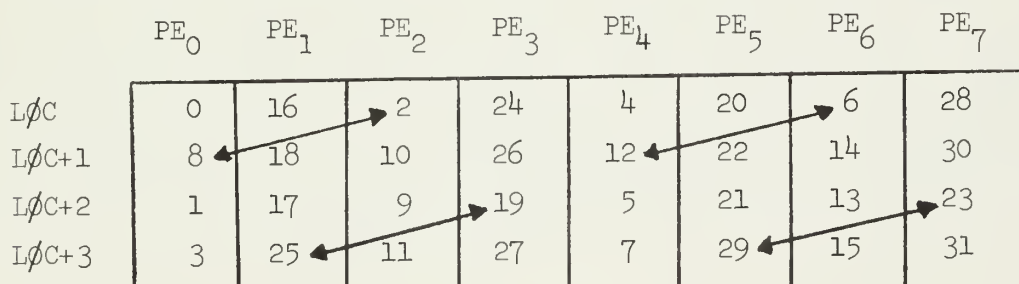
Since ILLIAC IV has 64 PE's it can scramble a folded data set of 4096 points 100% efficiently. The algorithm has been extended to handle small data sets.

Consider the following example of scrambling a 32 point data set in an 8 PE ILLIAC. The initial state would be:



Now perform the transfers indicated. It should be apparent that these operations take four rather than two routes since each group of four PE's cannot be treated separately in an end around fashion. Also these transfers cannot be 100% efficient since more than one routing distance is involved.

The result of the transfers would be:



Now only the above indicated transfers are required to complete the scrambling process. Note that the process is simply two applications of the previous method performed in parallel. The final result is:

	PE ₀	PE ₁	PE ₂	PE ₃	PE ₄	PE ₅	PE ₆	PE ₇
LφC	0	16	8	24	4	20	12	28
LφC+1	2	18	10	26	6	22	14	30
LφC+2	1	17	9	25	5	21	13	29
LφC+3	3	19	11	27	7	23	15	31

A generalized method for scrambling data which is folded in memory can now be given. Data is first scrambled in groups which are composed of as many PE's as there are rows of data by the method developed for "square" data sets (referring to N² data points in N PE's). Finally, consider these "squares" as units and scramble their position. One further example will make this last step more clear. Consider 16 data points in an 8 PE array.

First iteration

	PE ₀	PE ₁	PE ₂	PE ₃	PE ₄	PE ₅	PE ₆	PE ₇
LφC	0	1	2	3	4	5	6	7
LφC+1	8	9	10	11	12	13	14	15

(Note: In the original image, arrows indicate data movement from PE₁ to PE₀, PE₃ to PE₂, PE₅ to PE₄, and PE₇ to PE₆.)

The heavy lines below indicate the second step which completes the scrambling, giving the proper result.

	PE ₀	PE ₁	PE ₂	PE ₃	PE ₄	PE ₅	PE ₆	PE ₇
LφC	0	8	2	10	4	12	6	14
LφC+1	1	9	3	11	5	13	7	15

(Note: In the original image, heavy lines are drawn under the columns for PE₂, PE₃, PE₄, and PE₅. A double-headed arrow is drawn below these four columns, indicating a swap between the first two and last two columns of this group.)

Result

	PE ₀	PE ₁	PE ₂	PE ₃	PE ₄	PE ₅	PE ₆	PE ₇
LØC	0	8	4	12	2	10	6	14
LØC+1	1	9	5	13	3	11	7	15

Although the present implementation allows for only up to 4096 data points, larger data sets could be handled and it is easily seen that scrambling would be 100% efficient, but the core restrictions on the size of the data set and the size of the program would eventually limit the program. A short discussion of these restrictions is included under future considerations.

Future Considerations

Various extensions to this subroutine have been explored. Some of them seem impractical while others are quite worthy of further investigation and perhaps implementation. There also has been a suggestion that a separate subroutine be written to handle several parallel transforms, each with all of its data within one PE. If all transforms could be done in groups of 64 this would appear to be a very efficient method; however, if groups of data are not even multiples of 64, some PE's will be completely idle during the whole process. It is this author's opinion that such a subroutine would be very straightforward to code and would have enough value to be worth programming at some future time.

The question of expanding the present subroutine to accept data sets larger than 4096 sample is largely decided by available core. There does not seem to be too much demand for greater capability at this time and the program itself already consumes approximately 2,500 words of memory, mostly composed of stored constants. Each increase of a power of two in the size of data sets transformable will increase the size of the subroutine by almost a factor of two. Such increases can only be justified by a large demand for that capability. If and when expansion is warranted, the process would be straightforward with the possible restrictions of a 64K limit on

the size of the data set so that it could be completely core contained. Problems with formatting and accessing disk files tend to be formidable.

It has been shown by Robinson [4] that two real data sets can be handled simultaneously as if they composed one complex data set of the same size. It was suggested that capability be added for handling real data in this manner, with hopes of a factor of two increase in efficiency. The algorithm was studied and appears simple enough but applying it to a parallel structure like ILLIAC IV, does not seem feasible at this time.

ILLIAC IV FAST FOURIER TRANSFORM
USERS MANUAL

This report is intended to be a description of how to use the Fast Fourier Transform subroutine and exactly what it can do. An explanation of the Fourier Transform and of how the subroutine works on ILLIAC IV is given in ILLIAC IV Report #226 entitled "A Fast Fourier Transform Subroutine for ILLIAC IV." Also, copies of the assembly language listing of the subroutine are available from the Center for Advanced Computation and the University of Illinois.

The Fast Fourier Transform is used to transform a discrete time series of data points into a discrete frequency spectrum. Both input and output data values are represented as complex numbers in the following way: each number occupies one 64-bit word using the 32-bit floating point configuration where the real part of each number occupies the inner 32 bits. When the input series consists of only real numbers the numbers should be stored only in the outer portion of each word and the inner portions should be set to zero. In any case the output will always consist of complex numbers and will be stored in the same locations as the input, thus "overwriting" the input.

Due to the structure of the Fast Fourier Transform algorithm all input sequences must contain a number of data points which is an even power of two. The sizes of data sets which can be transformed by the ILLIAC IV subroutine are: 8, 16, 32, 64, 128, 256, 512, 1024, 2048, 4096. Other sizes of data sets can be transformed by "padding" them with zeros on the end until their size is in an even power of two between 8 and 4096 inclusive. The result of such a transform will be correct with only the frequency spacing of the result being effected.

This subroutine uses the standard subroutine linking conventions as described in "The ILLIAC IV Software Reference Manual." When referenced from a GLYPNIR program, a standard subroutine call statement can be used. When referenced from an assembly language program, the standard call macro can be used. This macro and an example of its use is listed in Appendix A. The Fast Fourier Transform subroutine requires three parameters named size, number, and pointer. These parameters have the following significance.

SIZE

Size gives the number of data points present in each input time series. Size must be an integer and should be an even power of two between 8 and 4096 inclusive. If it is not an even power of two, the next greatest power

of two will be assumed and will determine the size of the transform and the number of results returned. Note that the data must still be "padded" with zeros for the result to be meaningful. If the size specified is zero or greater than 4096, an error condition will be indicated and no transform will be performed. An error is indicated by filling ACAR2 with all ones.

NUMBER

Number must also be an integer specifying the number of transforms to be performed during this call to the subroutine. Any number of transforms can be performed as long as they are all the same size and as long as all of the input data is available in one continuous block of memory. If number is set to zero, no transform will be performed and ACAR2 will be set to all ones.

POINTER

Pointer is a 24-bit CU address giving the location of the first word of a continuous block of memory which contains all of the input data for this call of the transform subroutine. Pointer will also address the first word of the resulting frequency spectrum(s) when the subroutine is exited. This pointer is automatically set up by the calling macro which is exhibited in Appendix A.

The actual subroutine is contained in a file called FASTFOURIER/TRANSFORM which is available on a system tape of the subroutine library. This file must be properly linked to the calling program. The subroutine is called by using the entry identifier FFT.

INVERSE TRANSFORM

For performing inverse transforms the only change required is to call by using the entry identifier FFTINV.

APPENDIX A

Standard call macro from "The ILLIAC IV Software Reference Manual"

```
DEFINE CALL &NAME(&PARAMETERS)=  
    &IF &SIGN(&MFIELD (&NAME))  
        &THEN EXTERNAL &NAME; &FI
```

```
BEGIN BLOCK  
    BEGIN USE 63  
    LIST: DATA &PARAMETERS;  
    END;  
    CLC(2);  
    SLIT(2) LIST;
```

```
END;  
CLC(3);  
SLIT(3) &NAME;  
EXCHL(3) $ICR; ##;
```

Sample calling sequence to execute two 138 point transforms.

```
CALL FFT(HOWBIG, HOWMANY, WHERE);  
.  
.  
.  
WHERE: DATA [128 words of data];  
HOWBIG: DATA 64;  
HOWMANY: DATA 2;
```

APPENDIX B
Demonstration Run

This appendix compares a Fourier Transform performed on a Univac 1108 and the same transform performed on the ILLIAC IV simulator. The information is presented in the form of actual computer listing including the following: 1) A listing of the 256 real input data points. 2) The results of the Univac run (real part first followed by imaginary part). 3) The main program used to call the FFT routine for the ILLIAC IV simulation run. 4) The results of the simulation run. Differences in the two results are due primarily to the difference of the word size used in each case.

Input Data

0.	0.	-56.	-24.	4822.	-13007.	-9326.	5835.
17167.	12047.	3.	-696.	-1676.	7734.	-2215.	-7602.
10211.	165.	-17335.	9888.	-12968.	2744.	14245.	10537.
-11413.	14762.	4049.	3069.	17389.	6574.	18416.	507.
10625.	2364.	-3213.	-3609.	-6436.	-11208.	3291.	7014.
-7760.	-3226.	-16879.	12173.	-6848.	-8584.	7979.	13897.
1273.	-1518.	11723.	3709.	768.	-7109.	3905.	-17075.
17524.	-6860.	8704.	1967.	-4606.	-11202.	16318.	3432.
4807.	-19886.	-6603.	-11205.	-11151.	762.	7412.	951.
3167.	3270.	11954.	3003.	-1335.	-14578.	17089.	-845.
-6565.	-12545.	4537.	5618.	-5144.	-13695.	10170.	-3683.
-8762.	-924.	-2686.	4718.	-4320.	-5671.	-11186.	12147.
-651.	11049.	-12576.	3387.	1053.	-12898.	5476.	1719.
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Real Univac 1108 Result

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-.23935650+05	-.88190126+05	-.79698125+05	-.32208377+05	.12423822+06
.10475777+06	.12018498+06	-.89308050+05	-.31424301+05	-.18619060+04
.40882889+05	-.92820702+05	-.11257891+05	-.14610218+05	-.12452422+06
-.79964560+04	-.21300992+05	.11725652+05	-.11268548+06	.68437002+04
.19196831+06	.36610626+05	.12051347+06	.46726594+05	-.14447113+05
-.88314708+05	.60155586+05	-.29508836+05	.10651084+06	.62851124+05
.54568208+05	.10175734+06	-.81431963+04	-.35035923+04	.59602295+02
.50177875+05	.23466077+05	-.65641566+05	-.11844547+06	-.12877611+06
.62304680+05	.12676695+06	.95429297+04	-.22496512+05	-.12558822+06
-.75575159+05	-.50596355+05	-.94544403+05	.23490922+05	-.11136791+06
.36708912+04	.11267843+06	-.17295822+06	.24522155+05	.18504945+05
.86965552+05	.57614010+05	.17787883+06	-.13490052+06	-.10801400+06
.54710958+05	.28340147+05	-.28900264+06	-.70621265+05	.95541438+05
-.72483458+05	.93026729+05	-.42655679+05	-.87760739+05	.55541602+05
-.66131058+05	-.12314756+06	-.46119934+05	.63107815+05	-.35125625+05
.11841725+06	.21500096+06	.13246392+06	.18619414+05	-.12489943+06
.41877700+05	-.19586924+06	.35511073+05	-.18944333+06	-.49808327+05
-.10685043+06	.39295759+05	-.34118988+05	.87388329+05	-.56760156+05
.10627192+06	.17858835+05	-.61932731+05	.48561633+05	-.11576040+06
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-.86787733+05	.63162532+05	-.84544034+05	.14108025+06	-.25588855+05
.67912168+04	.62923921+05	-.22494207+06	.12123530+06	.59887308+05
.19538353+06	-.19840547+04	.59866334+05	-.42466545+05	.19907523+06
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.39295759+05	-.10685043+06	-.49808327+05	-.18944333+06	.35511073+05
-.19586924+06	.41877700+05	-.12489943+06	.18619414+05	.13246392+06
.21500096+06	.11841725+06	-.35125625+05	.63107815+05	-.46119934+05
-.12314756+06	-.66131058+05	.55541602+05	-.87760739+05	-.42655679+05
.93026729+05	-.72483458+05	.95541438+05	-.70621265+05	-.28900264+06
.28340147+05	.54710958+05	-.10801400+06	-.13490052+06	.17787883+06
.57614010+05	.86965552+05	.18504945+05	.24522155+05	-.17295822+06
.11267843+06	.36708912+04	-.11136791+06	.23490922+05	-.94544403+05
-.50596355+05	-.75575159+05	-.12558822+06	-.22496512+05	.95429297+04
.12676695+06	.62304680+05	-.12877611+06	-.11844547+06	-.65641566+05
.23466077+05	.50177875+05	.59602295+02	-.35035923+04	-.81431963+04
.10175734+06	.54568208+05	.62851124+05	.10651084+06	-.29508836+05
.60155586+05	-.88314708+05	-.14447113+05	.46726594+05	.12051347+06
.36610626+05	.19196831+06	.68437002+04	-.11268548+06	.11725652+05
-.21300992+05	-.79964560+04	-.12452422+06	-.14610218+05	-.11257891+05
-.92820702+05	.40882889+05	-.18619060+04	-.31424301+05	-.89308050+05
.12018498+06	.10475777+06	.12423822+06	-.32208377+05	-.79698125+05
-.88190126+05	-.23935650+05	-.43274833+05	.12878585+06	.30633514+05
.16432637+06				

Imaginary Univac 1108 Result

.00000000	-.32348535+03	-.15282431+06	-.27383049+05	.79234054+05
.12539749+05	.94287190+05	.34464606+05	-.41893783+05	.60283592+05
-.71297144+05	-.15175059+05	-.96626729+05	.51172803+05	-.20518874+05
.18855686+06	.29928419+05	.47544088+05	.49069532+05	-.76239434+05
.17435297+06	.21966100+05	-.10578316+06	.11941393+05	-.32980707+05
.35799352+05	-.12412578+05	-.48219366+05	.46967453+05	.36626935+05
-.62671231+05	.14840741+06	.64964977+05	-.17344318+06	-.21326760+05
-.11224059+06	.46812337+05	-.10760260+06	-.13977412+06	.45146798+05
-.92065512+04	-.88596548+05	.44764577+05	.47270071+05	.94768170+05
-.83311300+05	-.72885594+05	.95072141+05	.24357188+06	-.72368868+05
-.21239087+05	-.13493994+04	.19846301+06	.31240915+05	.44790895+05
-.78648652+04	.12952341+06	.93203335+05	.14410096+06	.10436177+06
-.11474280+06	.10571027+06	.23333460+05	-.32214113+05	.23472000+06
.10789059+06	-.33982734+05	-.15458620+05	-.24644316+06	.28471246+05
-.57622153+05	-.44369019+05	.35402116+05	-.31654141+06	.17098704+05
.18087399+05	.88054139+05	.15852791+06	-.22730289+06	-.47605405+04
-.17384096+06	.50385053+05	.23991060+05	-.13077045+06	-.63503576+05
-.97109678+05	.15650693+06	-.79624333+05	.10901463+06	-.19423950+05
-.38454852+05	.20013107+06	-.25320520+05	-.47217832+05	-.48101416+04
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.44369019+05	.57622153+05	-.28431246+05	.24644316+06	.15458620+05
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-.46812337+05	.11224059+06	.21326760+05	.17344318+06	-.64964977+05
-.14840741+06	.62671231+05	-.36626935+05	-.46967453+05	.48219366+05
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.32348535+03				

Main Program of ILLIAC IV Simulation

(Data stored in external file referenced by ABC)

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NU:                              DATA      1;
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                                SETEL      E.OR.-E;
                                SLIT(2)    ARGLIST;
                                CLC(3)
                                SLIT(3)    FFT;
                                EXCHL(3)   $ICR;
                                LIT(0)     1.ABC+600 .ABC;
                                DISPLAYR   $CO.20:8;
                                HALT;
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(OUTER)=+6611039+05
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 (OUTER)=-.796981178+05
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 (INNER)=-.273827348+05

APPENDIX C

Timing Summary †

SIZE OF DATA SET	TIME FOR SCRAMBLING	TIME FOR FFT	TOTAL	$\frac{N \log N}{64} \times 3\mu$ *
4096	75.0 μ sec	2233.6 μ sec	2308.6 μ sec	2304 μ sec
2048	48.4 μ sec	1038.4 μ sec	1096.8 μ sec	1056 μ sec
1024	53.1 μ sec	480.0 μ sec	533.1 μ sec	480 μ sec
512	23.2 μ sec	220.4 μ sec	243.4 μ sec	216 μ sec
256	17.4 μ sec	100.4 μ sec	117.8 μ sec	96 μ sec
128	10.5 μ sec	45.35 μ sec	55.85 μ sec	42 μ sec
64	4.65 μ sec	20.6 μ sec	25.25 μ sec	18 μ sec
2 x 32	2.80 μ sec	15.65 μ sec	18.45 μ sec	15 μ sec
4 x 16	2.40 μ sec	11.10 μ sec	13.50 μ sec	12 μ sec
8 x 8	1.75 μ sec	6.75 μ sec	8.50 μ sec	9 μ sec
Overhead			13 μ sec	

* This formula gives a rough estimate of the time required to do the FFT portion where each complex multiply operation takes 3 μ sec.

† All times base on 50 n sec clock.

APPENDIX D

SUBROUTINE LISTING

LISTED ON: 08/13/71 AT: 09:14

BEGIN COMMENT

THE FAST FOURIER TRANSFORM SUBROUTINE IS COMPOSED OF FOUR SEPARATE LOAD MODULES. THE FIRST IS THE MAIN PROGRAM WHICH HANDLES THE SUBROUTINE LINKAGE AND SETS UP THE CU REGISTERS. NEXT COMES THE SCRAMBLING ROUTINE WHICH REORDERS THE INPUT DATA SO THAT IT CAN BE TRANSFORMED. THEN COMES THE TRANSFORM ROUTINE WHICH COMPLETES THE TRANSFORM, PERFORMS AN INVERSE IF DESIRED, AND RETURNS CONTROL TO THE CALLING PROGRAM. FINALLY ALL OF THE CONSTANTS USED FOR THE TRANSFORM ARE COMPILED IN A SEPARATE DATA FILE.

THREE PARAMETERS ARE REQUIRED BY THIS PROGRAM. THEY ARE LISTED BELOW IN THE ORDER IN WHICH THEY MUST BE SUPPLIED.

- 1) SIZE - THE NUMBER OF DATA POINTS PER DATA SET.
- 2) NUMBER - THE NUMBER OF DATA SETS TO BE TRANSFORMED.
- 3) POINTER - THE CU(24 BIT) ADDRESS OF THE FIRST LOCATION OF DATA.

A USERS MANUAL FOR THIS SUBROUTINE HAS BEEN PUBLISHED AND IS TITLED "THE ILLIAC IV FAST FOURIER TRANSFORM SUBROUTINE USERS MANUAL" BY JAMES E. STEVENS JR.

THE MAIN PORTION OF THE PROGRAM FOLLOWS AND SETS UP CU REGISTERS WITH CONTROL INFORMATION AS SHOWN BELOW.
LOCAL DATA BUFFER (LDB OR ADB)

D0(PATTERN)

IN THE SCRAMBLE SECTION OF THE PROGRAM THIS REGISTER IS USED FOR SAVING MODE PATTERNS

D1(FLAG)

THIS REGISTER IS DIVIDED INTO THREE FIELDS. THE FIRST BIT NUMBER 15, IS USED TO INDICATE WHETHER AN INVERSE TRANSFORM HAS BEEN REQUESTED. IF BIT 15 IS SET TO ONE AN INVERSE TRANSFORM WILL BE PERFORMED. THE SECOND FIELD USES THE NEXT 24 BITS AND IS SIMPLY A COPY OF THE FIRST PARAMETER, SIZE, WHICH GIVES THE NUMBER OF DATA POINTS IN EACH INPUT DATA SET. THE VARIABLE, SIZE, MUST BE AN EVEN POWER OF TWO. IF THE PARAMETER, SIZE, PASSED TO THE SUBROUTINE IS NOT A POWER OF TWO IT WILL BE ASSUMED THAT THE NEXT LARGEST POWER OF TWO IS DESIRED AND THE PROPER VALUE IS THEN GENERATED. THE FINAL 24 BIT FIELD OF THE FLAG REGISTER IS LOADED WITH THE PF ADDRESS OF THE FIRST LOCATION OF THE DATA SET TO BE TRANSFORMED. THIS ADDRESS IS EQUAL TO THE THIRD PARAMETER, POINTER, DIVIDED BY 64 SO IT IS A PF ADDRESS.

LATER, IN THE TRANSFORM SECTION OF THIS PROGRAM, THE INFORMATION IN REGISTER D1(FLAG) IS LOADED INTO ACAR0 AND REFERRED TO BY THE NAME SIZE.

D2(RETURN)

THIS REGISTER IS USED TO SAVE THE RETURN ADDRESS FOUND IN ACAR3 WHEN THE SUBROUTINE IS ENTERED. THE ACTUAL RETURN ALSO USES ACAR3. AN ERROR RETURN IS FLAGGED BY FILLING ACAR2 WITH ALL ONES.

D3 AND D4

SAVE AREA FOR ACAR0 AND ACAR1.

ACAR0(SIZE,INDEX)

IN THE MAIN SECTION OF THE SUBROUTINE THIS REGISTER IS LOADED WITH THE VALUE SIZE (THE FIRST PARAMETER). AFTER THE SIZE IS USED

TO MAKE A CONDITIONAL BRANCH THIS REGISTER IS USED TO HOLD INDEXING INFORMATION DURING THE SCRAMBLING PROCESS. AS NOTED ABOVE, FOR THE TRANSFORM SECTION OF THIS PROGRAM THIS REGISTER IS LOADED FROM REGISTER 01(FLAG) AND REFERRED TO AGAIN BY THE NAME SIZE.

ACAR1(MODE)

THIS REGISTER IS NOT FILLED WITH ANY INITIAL VALUE AND IS USED THROUGHOUT THE SUBROUTINE FOR SAVING MODE PATTERNS FOR ENABLING PEG

ACAR2(LOOP)

THIS REGISTER IS NOT FILLED WITH ANY INITIAL VALUE AND IS USED THROUGHOUT THE SUBROUTINE FOR LOOP CONTROL INFORMATION.

ACAR3(COUNT)

THIS REGISTER CONTAINS A POINTER TO THE ROW OF DATA BEING CONSIDERED, AS WELL AS A POINTER TO THE LAST ROW OF DATA (A LIMIT). IT IS USED TO INDEX PE MEMORY AND TO DETERMINE WHEN THE TRANSFORM IS COMPLETE. THIS REGISTER IS SET UP BY THE MAIN SECTION OF THE SUBROUTINE AS FOLLOWS: (1) THE INCREMENT FIELD IS SET TO ONE. (2) THE LIMIT FIELD IS SET TO THE ADDRESS OF THE FIRST ROW OF DATA PLUS THE TOTAL NUMBER OF ROWS SUPPLIED (=THE SIZE OF THE DATA SETS TIMES THE NUMBER OF DATA SETS DIVIDED BY 64). (3) THE ADDRESS FIELD (LAST 24 BITS) IS SET TO THE PE ADDRESS OF THE FIRST ROW OF DATA:

EXTERNAL	HERE:	
FILL	16:	ALIGNMENT FOR NEXT THREE STATEMENTS.
FFTENLOCY1: SLIT(0)	0:	ASET FLAG FOR NO INVERSE.
SKIP	01:	SKIP NEXT STATEMENT.
FFTINVLENTY1: SLIT(0)	1:	ASET FLAG FOR INVERSE.
STL(0)	003:	SAVE ACAR0.
STL(1)	004:	SAVE ACAR1.
STL(3)	002:	SAVE RETURN ADDRESS.
LOAD(2)	003:	FETCH FIRST ARGUMENT ADDRESS.
LOAD(3)	003:	FETCH FIRST ARGUMENT *SIZE*.
ALIT(3)	71:	FIND THE SMALLEST POWER OF TWO
CSHL(3)	12:	GREATER THAN OR EQUAL TO SIZE.
LEAD(3)	:	WHAT POWER IS IT.
GTSR(3)	55,ERR:	IF ZERO, RETURN AND FLAG ERROR.
CR(3)	55:	ERASE LEAD ONE FLAG.
SLIT(1)	63:	ELSE, SUBTRACT BIT NO FROM 63
CSHR(1)	003:	GIVING THE PROPER POWER.
SLIT(3)	=1:	RECONSTRUCT SIZE
CSHL(3)	0(1):	ARY SHIFTING.
CSHL(0)	24:	SHIFT FLAG.
CADD(0)	003:	ADD SIZE TO FLAG REGISTER.
ALIT(2)	1:	INCREMENT ARG POINTER.
LOAD(2)	003:	FETCH SECOND ARGUMENT ADDRESS.
LOAD(3)	003:	FETCH SECOND ARGUMENT *NUMBER*.
ZERT(3)	0,ERR:	IF ZERO RETURN AND FLAG ERROR.
CSHL(3)	0(1):	MULT NO BY SIZE AND DIV BY 64.
ALIT(2)	1:	INCREMENT ARG POINTER.
LOAD(2)	002:	FETCH THIRD ARGUMENT *POINTER*.
CSHR(2)	0:	DIV BY 64. (MAKE PE ADDRESS).
CSHL(0)	24:	SHIFT FLAG REGISTER.
CADD(0)	002:	ADD POINTER TO FLAG REGISTER.
STL(0)	001:	SAVE FLAG REGISTER.
CSHR(0)	24:	LEAVE SIZE IN ADDR FLD OF 000.
CADD(3)	002:	SET COUNT LIMIT.
ALIT(3)	=71:	ONE LESS FOR LOOP CONTROL.
CRTR(3)	24:	POSITION INCREMENT FIELD.
SLIT(3)	=1:	SET INCREMENT TO ONE.

```

CRCTR(3)      16;
SLIT(3)       =0;
CADD(3)       $C2;
SETE          F.DR.-E;
SETE1         E.DR.-E;
CHWS          64;
JUMP          HERE;
CLC(2)        ;
CMPCC(2)      ;
LDL(0)        $D3;
LDL(1)        $D4;
LDL(3)        $D2;
EXCHL(3)     $ICR;
END           FFT.

```

```

*POSITION ADDRESS FIELD.
*ZERO OUT ADDRESS FIELD.
*SET ADDRESS FIELD TO POINTER.
*TURN ON
&          ALL PES.
*WORD SIZE TO 64.
*GO TO SCRAMBLING ROUTINE
*ERROR (NO SIZE GIVE).
*SET ACAR 2 TO ALL ONES.
*RESTORE ACAR0.
*RESTORE ACAR1.
*FETCH RETURN ADDRESS.
*RETURN TO CALLING PROGRAM.

```

ERR:

```

      BEGIN
X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X
X  X
X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X  X

```

```

      EXTERNAL      FFT1,PEINDEX;
SIZE:      REGC      XSIZE=300;
            EQU       0;
            REGC      XINDEX=300;
INDEX:     EQU       0;
            REGC      XMODE=301;
MODE:      EQU       1;
            REGC      XLOOP=302;
LOOP:      EQU       2;
            REGC      XCOUNT=303;
COUNT:    EQU       3;
            REGC      XPATTERN=304;

```

```

X
X
X      THE PE MEMORY LOCATION PEINDEX CONTAINS A SET OF SIX INDICES
X      FOR EACH PE PACKED INTO ONE WORD(SIX BITS PER INDEX, 36 TOTAL).
X      THE DEFINE NAMED FETCHINDEX WHICH FOLLOWS MASKS OFF BITS FROM
X      PEINDEX AND STORES THE RESULT IN REGISTER X AND REGISTER S. THE
X      PARAMETER TO THE DEFINE DETERMINES WHICH BITS OF PEINDEX ARE TO
X      BE FETCHED.
X

```

```

DEFINE  FETCHINDEX      XSTART: XHOWMANY:=
      LDA      PEINDEX;      XFETCH PACKED INDICES.
      SHARR    63-XSTART;    XALIGN TO STARTING BIT POSITION.
      CLRA
      SHARL    XHOWMANY;     XRECOVER BITS OF INTEREST.
      LDS      XA;           XINDEX TO REG S AND REG X.
      LDX     XS:#0;

```

```

X
X
X      EACH OPERATION OF THE SCRAMBLING ROUTINE INVOLVES ROUTING DATA
X      FROM A STARTING LOCATION IN ONE PE TO A DESTINATION IN ANOTHER PE,
X      AND RETURNING THE PREVIOUS CONTENTS OF THE DESTINATION TO THE
X      STARTING LOCATION. INTERCHANGE IS A DEFINE WHICH ACCOMPLISHES THIS
X      IN ALL PEs SIMULTANEOUSLY WHEN SUPPLIED WITH THE FOLLOWING INFORMA-
X      TION. REGISTERS X AND S CONTAIN INDICES FOR STARTING AND DESTINA-
X      TION LOCATIONS RESPECTIVELY. THE TRANSFERS ARE PATTERNED IN SUCH
X      A WAY THAT THEY CAN BE ITERATED BY SIMPLY ROUTING THE INDICES.
X      THE CU MODE REG IS ASSUMED TO CONTAIN THE PROPER ENABLING PATTERN.
X      THE ONE INPUT PARAMETER REQUIRED IS THE NUMBER OF ROWS OF STORAGE
X      COMPOSING THE DATA SET. THIS DEFINE WILL ONLY BE USED IN SCRAMBL-
X      ING DATA SETS LARGER THAN 128, AND IT COMPLETES THE PORTION OF
X      SCRAMBLING DEALING WITH SQUARE PARTITIONS AS DESCRIBED IN THE FFT
X      DOCUMENT OF SEPTEMBER 1970. ALL PEs ARE ASSUMED ENABLED.
X

```

```

DEFINE  INTERCHANGE     XNDI :=
      BEGIN BLOCK
      STL(MODE)          XSAVE MODE PATTERN.
      LIT(LOOP)         XLOOP COUNTER.
      LIT(INDEX)        XROUTING INDEX(=LOOP COUNT).
LOOP:   RTL             XDESTINATION INDEX.
      LDS              XCOMPLETE S TO S TRANSFER.
      RTL             XSTARTING INDEX.
      LDX             XCOMPLETE X TO X TRANSFER.

```



```

LDR      +0(COUNT);      %FETCH STARTING DATA.
RTL      0(LOOP);        %ROUTE.
LDA      %R;              %SAVE STARTING DATA.
RTL      -&NUM;           %FAKE END AROUND ROUTE (RIGHT).
LDEE1    %MODE;          %END AROUND BY &NUM PES.
LDA      %R;              %FILL IN STARTING DATA.
SETE     E.0R.-E;        %ENABLE ALL.
SETE1    E.0R.-E;
LDR      +0(COUNT);      %FETCH DESTINATION DATA.
STA      +0(COUNT);      %STORE STARTING DATA IN DEST.
RTL      0(INDEX);       %ROUTE DESTINATION DATA BACK.
LDA      %R;              %SAVE DESTINATION DATA.
RTL      &NUM;           %FAKE END AROUND ROUTE (LEFT).
CROTR(MODE) %NUM(INDEX); %END AROUND BY &NUM PES.
LDEE1    %MODE;
LDA      %R;
SETE     E.0P.-E;        %FILL IN DESTINATION DATA.
SETE1    E.0P.-E;        %ENABLE ALL.
STA      +0(COUNT);      %STORE DEST. DATA IN START LOC.
ALIT(INDEX)  =-1;        %INCREMENT INDEX (=LOOP COUNT).
CROTL(MODE) %NUM(INDEX); %RESTORE MODE PATTERN (ALMOST).
COR(MODE)  %PATTERN;     %MODIFY MODE PATTERN.
TXLTM(LOOP) %DL00P;     %RETURN TO LOCAL 0LOOP(INC LOOP.
RTL      %S,1;           %DESTINATION INDEX.
LDS      %R;              %COMPLETE S TO S TRANSFER.
LDR      +0(COUNT);      %FETCH START AND DEST. DATA.
RTL      &NUM/2;         %EXCHANGE START AND DEST. DATA.
LDA      %R;              %SAVE.
RTL      -&NUM;           %FAKE END AROUND ROUTE OF &NUM/2.
LDEE1    %MODE;          %END AROUND BY &NUM PES.
LDA      %R;              %FILL IN DESTINATION DATA.
SETE     E.0P.-E;        %ENABLE ALL.
SETE1    E.0P.-E;
STA      +0(COUNT);      %STORE EXCHANGED START AND DEST.
END      ;##;

```

*
*
*
*
*
*
*
*
*
*
*

FOR THE SMALLER DATA SETS (64 AND SMALLER) SCRAMBLING CAN BE ACCOMPLISHED WITH ONLY ONE ACCESS TO ONE ROW OF STORAGE. THE INTERCHANGE OPERATION CAN BE DONE USING ONLY REGISTER TRANSFERS AND ROUTING. EXCHANGE IS A DEFINE WHICH PERFORMS ONE INTERCHANGE ON DATA FOUND IN REGISTER A. THE DISTANCE SEPARATING THE TWO WORDS TO BE INTERCHANGED IS THE ONLY PARAMETER. A PROPER MODE PATTERN MUST BE SUPPLIED IN THE CURRENT REGISTER.

```

DEFINE  EXCHANGE      %NUM;=
LDR      %A;           %DATA TO REGISTER R.
RTL      %NUM;         %ROUTE STARTING DATA.
LDEE1    %MODE;       %SELECT ELEMENTS TO BE EXCHANGED.
LDS      %A;           %PERFORM THE
LDA      %R;           %EXCHANGE.
RTL      %S,-%NUM;    %ROUTE BACK TO THE START.
CSHL(MODE) %NUM;     %POSITION MODE PATTERN TO
LDEE1    %MODE;       %FILL IN DESTINATION DATA.
LDA      %R;##;

```

*
HERE[ENTRY]::

```

CTSRT(SIZE)  60,x8;
CTSRT(SIZE)  59,x16;
CTSRT(SIZE)  58,x32;
CTSRT(SIZE)  57,x64;

```


CTSRT(SIZE) 56,X128;
CTSRT(SIZE) 55,X256;
CTSRT(SIZE) 54,X512;
CTSRT(SIZE) 53,X1024;
CTSRT(SIZE) 52,X2048;
CTSRT(SIZE) 51,A4096;

CLC(2) ;
COMPC(2) ;
LDL(0) \$03;
LDL(1) \$04;
LDL(3) \$02;
EXCHL(3) TIGR;

*ERROR. DATA SIZE TOO LARGE.
*SET ACAR2 TO ALL ONES.
*RESTORE ACAR0.
*RESTORE ACAR1.
*FETCH RETURN ADDRESS.
*RETURN.

X8: JUMP A8;
X16: JUMP A16;
X32: JUMP A32;
X64: JUMP A64;
X128: JUMP A128;
X256: JUMP A256;
X512: JUMP A512;
X1024: JUMP A1024;
X2048: JUMP A2048;

*
*
*

THIS PORTION OF THE PROGRAM REORDERS (SCRAMBLES) THE DATA IN SUCH A WAY THAT THE RESULTS OF THE TRANSFORM ROUTINE WILL COME OUT SEQUENTIALLY STORED IN PE MEMORY. ONLY ONE OF THE FOLLOWING TEN SECTIONS OF CODE WILL BE EXECUTED DEPENDING ON THE SIZE OF THE DATA SETS. THE SCRAMBLING PROCESS IS PURELY A DATA TRANSFER PROBLEM AND IS ACCOMPLISHED QUICKLY (A FRACTION OF THE TRANSFORM TIME). THE PRIMARY OPERATIONS ARE MEMORY ACCESS AND ROUTING. THE OTHER INSTRUCTION SET UP INDEXING AND LOOP CONTROL.

A4096:: FETCHINDEX 5716; *DEFINE (FETCH FROM PEINDEX).
LIT(LOOP) 1,31,1; *LOOP COUNTER.
LIT(INDEX) =63; *ROUTING INDEX (=LOOP COUNT).
LOOP1: RTL \$S,1; *DESTINATION INDEX.
LDS \$R; *COMPLETE S TO S TRANSFER.
RTL \$X,-1; *STARTING INDEX.
LDX \$R; *COMPLETE X TO X TRANSFER.
LDR +0(COUNT); *FETCH STARTING DATA.
RTL 0(LOOP); *ROUTE. END AROUND BY 64
LDA #0(COUNT); *FETCH DESTINATION DATA.
STR #0(COUNT); *STORE STARTING DATA IN DEST.
RTL \$A,0(INDEX); *ROUTE DESTINATION DATA BACK.
STR #0(COUNT); *STORE DEST. DATA IN START LOC.
ALIT(INDEX) =-1; *INCREMENT INDEX (=LOOP COUNT).
TXLTM(LOOP) ,LOOP1; *RETURN TO LOOP1(INC LOOP COUNT)
RTL \$S,1; *DESTINATION INDEX.
LDS \$R; *COMPLETE S TO S TRANSFER.
LDR +0(COUNT); *FETCH START AND DEST. DATA.
RTL 32; *ROUTE END AROUND BY 64.
STR +0(COUNT); *STORE EXCHANGED START AND DEST.
ALIT(COUNT) =63; *INCREMENT COUNT.
TXLTM(COUNT) ,A4096; *GO BACK FOR NEXT DATA SET.
JUMP FFT1; *JUMP TO TRANSFORM ROUTINE.
A2048:: FETCHINDEX 5116; *DEFINE (FETCH FROM PEINDEX).
LIT(MDUF) =1000000000200000000018; *
INTERCHANGE 32; *DEFINE (LARGE).
ALIT(COUNT) =31; *INCREMENT COUNT.
TXLTM(COUNT) ,A2048; *GO BACK FOR NEXT DATA SET.

A1024::	JUMP	FFT1;	%JUMP TO TRANSFORM ROUTINE.
	FETCHINDEX	45:6;	%DEFINE (FETCH FROM PEINDEX).
	LIT(MODE)	=1000004000020000100000:8;	
	INTERCHANGE	16;	%DEFINE (LARGE).
LOOP2:	LIT(LOOP)	1:15:0;	%LOOP COUNTER.
	LDA	0(COUNT);	%FETCH DATA.
	LIT(MODE)	=377776000000:8;	
	EXCHANGE	16;	%DEFINE.
	SETE	E.0R.=E;	%ENABLE ALL.
	SETE1	E.0R.=E;	*
	STA	0(COUNT);	%STORE RESULT.
	ALIT(COUNT)	=1;	%INCREMENT COUNT.
	TXLTM(LOOP)	,LOOP2;	%GO BACK FOR NEXT ROW.
	TXLT(COUNT)	%COUNT,A1024;	%GO BACK FOR NEXT DATA SET.
	JUMP	FFT1;	%JUMP TO TRANSFORM ROUTINE.
A512::	FETCHINDEX	39:6;	%DEFINE (FETCH FROM PEINDEX).
	LIT(MODE)	=1002004010020040100200:8;	
	INTERCHANGE	8;	%DEFINE (LARGE).
	LIT(LOOP)	1:7:0;	%LOOP COUNTER.
LOOP3:	LDA	0(COUNT);	%FETCH DATA.
	LIT(MODE)	=37700177400:8;	
	EXCHANGE	24;	%DEFINE.
	SETE	E.0R.=E;	%ENABLE ALL.
	SETE1	E.0R.=E;	*
	STA	0(COUNT);	%STORE RESULT.
	ALIT(COUNT)	=1;	%INCREMENT COUNT.
	TXLTM(LOOP)	,LOOP3;	%GO BACK FOR NEXT ROW.
	TXLT(COUNT)	%COUNT,A512;	%GO BACK FOR NEXT DATA SET.
	JUMP	FFT1;	%JUMP TO TRANSFORM ROUTINE.
A256::	FETCHINDEX	33:6;	%DEFINE (FETCH FROM PEINDEX).
	LIT(MODE)	=1042104210421042104210:8;	
	INTERCHANGE	4;	%DEFINE (LARGE).
	LIT(LOOP)	1:3:0;	%LOOP COUNTER.
LOOP4:	LDA	0(COUNT);	%FETCH DATA.
	LIT(MODE)	=36074170360:8;	
	EXCHANGE	28;	%DEFINE.
	LIT(MODE)	=7760000000177400:8;	
	EXCHANGE	8;	%DEFINE.
	SETE	E.0R.=E;	%ENABLE ALL.
	SETE1	E.0R.=E;	*
	STA	0(COUNT);	%STORE RESULT.
	ALIT(COUNT)	=1;	%INCREMENT COUNT.
	TXLTM(LOOP)	,LOOP4;	%GO BACK FOR NEXT ROW.
	TXLT(COUNT)	%COUNT,A256;	%GO BACK FOR NEXT DATA SET.
	JUMP	FFT1;	%JUMP TO TRANSFORM ROUTINE.
A128::	FETCHINDEX	27:6;	%DEFINE (FETCH FROM PEINDEX).
	RTL	%S:1;	%ROUTE INDEX.
	LDS	%R;	%COMPLETE S TO S TRANSFER.
	LDA	0(COUNT);	%FETCH DATA.
	LIT(MODE)	=52525252525252525252:8;	
	EXCHANGE	1;	%DEFINE.
	SETE	E.0R.=E;	%ENABLE ALL.
	SETE1	E.0R.=E;	*
	STA	0(COUNT);	%STORE RESULT.
	LIT(LOOP)	1:1:0;	%LOOP COUNTER.
LOOP5:	LDA	0(COUNT);	%FETCH DATA.
	LIT(MODE)	=31463146314:8;	
	EXCHANGE	30;	%DEFINE.
	LIT(MODE)	=74177417001703770360:8;	
	EXCHANGE	12;	%DEFINE.
	SETE	E.0R.=E;	%ENABLE ALL.

```

A64!! SETE1      E.O.R.=E;      %
STA      0(COUNT);      %STORE RESULT.
ALIT(COUNT)  #1;      %INCREMENT COUNT.
TXLTM(LOOP) .LOOPS;      %GO BACK FOR OTHER ROW.
TXLT(COUNT) $COUNT,A128; %GO BACK FOR NEXT DATA SET.
JUMP      FFT1;      %JUMP TO TRANSFORM ROUTINE.
LDA      0(COUNT);      %FETCH DATA.
LIT(MODE)  #2525252525218;
EXCHANGE  31;      %DEFINE.
LIT(MODE)  #631460000014631418;
EXCHANGE  14;      %DEFINE.
LIT(MODE)  #360001700007400036018;
EXCHANGE  4;      %DEFINE.
SETE      E.O.R.=E;      %ENABLE ALL.
SETE1     E.O.R.=E;      %
STA      0(COUNT);      %STORE RESULT.
TXLTM(COUNT) .A64;      %GO BACK FOR NEXT DATA SET.
JUMP      FFT1;      %JUMP TO TRANSFORM ROUTINE.
A32!! LDA      0(COUNT);      %FETCH DATA.
LIT(MODE)  #525250000012525218;
EXCHANGE  15;      %DEFINE.
LIT(MODE)  #314001460006300031418;
EXCHANGE  6;      %DEFINE.
SETE      E.O.R.=E;      %ENABLE ALL.
SETE1     E.O.R.=E;      %
STA      0(COUNT);      %STORE RESULT.
TXLTM(COUNT) .A32;      %GO BACK FOR NEXT 2 DATA SETS.
JUMP      FFT1;      %JUMP TO TRANSFORM ROUTINE.
A16!! LDA      0(COUNT);      %FETCH DATA.
LIT(MODE)  #252001250005240025218;
EXCHANGE  7;      %DEFINE.
LIT(MODE)  #6014030060140300601418;
EXCHANGE  2;      %DEFINE.
SETE      E.O.R.=E;      %ENABLE ALL.
SETE1     E.O.R.=E;      %
STA      0(COUNT);      %STORE RESULT.
TXLTM(COUNT) .A16;      %GO BACK FOR NEXT 4 DATA SETS.
JUMP      FFT1;      %JUMP TO TRANSFORM ROUTINE.
A8!!  LDA      0(COUNT);      %FETCH DATA.
LIT(MODE)  #5012024050120240501218;
EXCHANGE  3;      %DEFINE.
SETE      E.O.R.=E;      %ENABLE ALL.
SETE1     E.O.R.=E;      %
STA      0(COUNT);      %STORE RESULT.
TXLTM(COUNT) .A8;      %GO BACK FOR NEXT 8 DATA SETS.

```

```

%
%
%
%
%
%
%
%
%

```

```

END      HERE.

```

```

      BEGIN
%  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %
%  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %
%  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %  %

```

```

      EXTERNAL      SINP
SIZE:  EQU          0;
      REGC          $SIZE=$C0;
MODE:  EQU          1;
      REGC          $MODE=$C0;
LOOP:  EQU          7;
      REGC          $LOOP=$C0;
COUNT: EQU        3;
      REGC          $COUNT=$C0;

```

```

%      THE FOLLOWING DEFINE IS A STANDARD TRANSFER OPERATION USED IN
%      THE FOURIER TRANSFORM ROUTINE.  THE PARTIAL RESULTS OF EACH ITERA-
%      TION ARE USED IN THE PE WHERE THEY WERE COMPUTED AS WELL AS IN A
%      PE SOME DISTANCE AWAY FOR THE NEXT ITERATION.  THE FUNCTION OF THIS
%      DEFINE IS TO MOVE THESE PARTIAL RESULTS(FOUND IN REGISTER A) TO THE
%      PROPER PES ($DIST IS A NUMBER TELLING HOW FAR).  THE PROPER MODE
%      PATTERN MUST BE SUPPLIED IN THE CU REGISTER,MODE($CARI).  NOTE THAT
%      REGISTER MODE ($CARI) IS NOT CHANGED BY THIS DEFINE.
%
%

```

```

DEFINE TRANSFER      $DIST;=
      RTL            $A,$DIST;      %ROUTE REGISTER A RIGHT.
      COMPC(MODE)    ;              %COMPLIMENT MODE PATTERN.
      LDEE1          $MODE;        %ENABLE PES.
      LDS            $R;           %LOAD REGISTER S IN DESIRED PES.
      RTL            $A,-$DIST;    %ROUTE REGISTER A LEFT.
      COMPC(MODE)    ;              %COMPLIMENT MODE PATTERN.
      LDEE1          $MODE;        %ENABLE PES.
      LDS            $A;           %LOAD REGISTER S IN DESIRED PES.
      LDA            $R;           %LOAD REGISTER A IN SAME PES.
      SETE           E.0R.-E;      %ENABLE ALL PES.
      SETE1          E.0R.-E;###;

```

```

% PAGE 1
%      THE FOLLOWING DEFINE PERFORMS THE COMPLEX MULTIPLICATION
%      OPERATION ON TWO OPERANDS.  EACH OPERAND IS ON WORD COMPOSED OF
%      TWO 32-BIT FLOATING POINT NUMBERS, THE OUTER OF WHICH IS THE REAL
%      PART AND THE INNER IS THE IMAGINARY PART.  THE TWO OPERANDS MUST
%      BE IN THE A AND S REGISTERS IN ALL PES.  THE RESULT IS LEFT IN
%      REGISTER A.  THE RESULTS IN TERMS OF R1(REAL PART OF FIRST OPERAND)
%      R2,I1,AND I2 ARE AS FOLLOWS.
%
%

```

$$R=R1*R2-I1*I2$$

$$I=R1*I2+R2*I1$$

```

DEFINE COMPLEXMULT  $WHI;=
      MLRN          $S;           %MULTIPLY REALS AND IMAGINARIES.
      LDR           $WHI;        %RELOAD SECOND OPERAND.
      SETE          E.AND.-E;    %
      CHSA          ;            %CHANGE SIGN OF INNER (I+2=-1).
      SETE          E.0R.-E;    %
      LDR           $S;           %MOVE OPERAND TO R REGISTER.
      LDS           $A;           %SAVE PARTIAL RESULT.
      LDA           $R;           %FIRST OPERAND TO A REGISTER.
      SWAPA         ;            %IMAG TO OUTER, REAL TO INNER.
      MLRN          $R;           %MULTIPLY R1*I2 AND R2*I1.
      LDR           $S;           %POSITION PARTIAL RESULTS

```



```

        SWAPX          ;
        ADN            SB;##
X      THE FOLLOWING DEFINE REQUIRES PROPER CONSTANTS TO BE FOUND
X      IN REGISTER S AND PRODUCES TWO RESULTS AND STORES THEM IN APPROPRI-
X      ATE MEMORY LOCATIONS. THE OFFSET USED TO FIND THE FIRST OPERAND
X      SHOULD BE EQUAL TO HALF THE NUMBER OF ROWS BEING OPERATED ON IN A
X      GIVEN ITERATION. THE SIGNIFICANCE OF THIS DEFINE FOLLOW IN BRIEF.
X      THE BASIC OPERATION OF THE FAST FOURIER TRANSFORM IS A COMPLEX
X      MULTIPLICATION BETWEEN THE RESULTS OF ONE ITERATION AND A SET OF
X      CONSTANTS (W S). THIS NEW QUANTITY IS THEN ADDED TO THE RESULTS OF
X      THE PREVIOUS ITERATION TO PRODUCE RESULTS FOR THE PRESENT ITERA-
X      TION. THE SYMETRY OF THE CALCULATIONS AS WELL AS THAT OF THE W S
X      ALLOWS HALF THE OPERATIONS TO BE ELIMINATED BY ADDING ONE SUBTRAC-
X      TION TO THE OTHER HALF. THUS A STANDARD DEFINE WHICH ACCOMPLISHES
X      ON COMPLEX MULTIPLY ONE ADD AND ONE SUBTRACT AND PRODUCES TWO RES.
X

```

```

DEFINE MULTADDSUB      &OFFSET;=
LDA                   &OFFSET(COUNT);%FETCH FIRST OPERAND,
COMPLEXMUL           &OFFSET(COUNT);%MULTIPLY BY CONSTANT(W) IN RGS.
LDR                   0(COUNT);      %FETCH SECOND OPERAND.
LDS                   %A;             %SAVE PARTIAL RESULT.
ADN                   %R;             %ADD 2ND OPERAND TO PARTIAL RES.
STA                   0(COUNT);      %STORE ONE RESULT.
LDA                   %R;             %2ND OPERAND TO REGISTER A.
SRN                   %S;             %SUBTRACT PARTIAL RESULT.
STA                   &OFFSET(COUNT);%} %STORE THE OTHER RESULT.

```

```

X      THE CONSTANTS (W S) FOR THIS PROGRAM ARE REPRESENTED AS SINES
X      AND COSINES RATHER THAN EXPONENTIALS. TO DECREASE THE NUMBER OF
X      CONSTANTS REQUIRED TO BE STORED AS PART OF THE PROGRAM ADVANTAGE
X      WAS TAKEN OF THEIR REGULARITY AND SYMETRY. THE SINES OR COSINES
X      OF ANGLES IN ANY OF THE FOUR QUADRANTS ARE SIMPLE TRANSFORMATION
X      OF THOSE OF ANGLES IN THE FIRST QUADRANT. FOR THIS REASON ONLY
X      FIRST QUADRANT SINES AND COSINES ARE STORED.

```

```

X      THE FOLLOWING DEFINE LOADS A SET OF CONSTANTS AND TRANSFORMS
X      THEM INTO THE PROPER SECOND QUADRANT VALUES. THE RESULTS OF THIS
X      DEFINE ARE LEFT IN REGISTER S AND THE MEMORY LOCATIONS OF THE
X      STORED CONSTANTS ARE NOT DISTURBED. THE INPUT PARAMETER IS ASSUMED
X      TO BE THE ADDRESS OF THE MEMORY LOCATION CONTAINING THE CONSTANTS
X      REQUIRED IN THE CURRENT ITERATION. THE TRANSFORMATION IS DEFINED:
X

```

```

REAL(QUAD 2)=-IMAGINARY(QUAD 1)
IMAGINARY(QUAD 2)=REAL(QUAD 1)

```

```

DEFINE CHANGEQUAD     &COMPLEXNUM;=
LDA                   &COMPLEXNUM;   %FETCH CONSTANTS.
SWAPA                  ;              %SWAP REAL AND IMAGINARY PARTS.
SETE1                  E.AND.-E;     %
CHSA                   ;              %CHANGE SIGN OF REAL PART.
SETE1                  E.OR.-E;      %
LDS                   %A;##          %LEAVE RESULT IN REGISTER S.

```

```

X      THE FOLLOWING DEFINE RESETS THE TRUE-FALSE FLIP-FLOP (TFFF)
X      WHICH IS USED TO CONTROL A SMALL LOOP.
X

```

```

DEFINE RESETFF       =
CACRB                 0;##          %RESET ACR REGISTER BIT 0 (TFFF)

```

```

X      THE FOLLOWING DEFINE SETS THE TRUE-FALSE FLIP-FLOP WHICH IS
X      USED TO CONTROL A SMALL LOOP.
X

```

```

DEFINE SETFF         =

```

```

CACRB          200:8 ##)      %SET ACR REGISTER BIT 0 (TFFF).
%
% AT THIS POINT SCRAMBLING HAS BEEN COMPLETED AND THE REAL WORK
% MUST BEGIN. THE TRANSFORM ALGORITHM BEGINS BY WORKING WITH GROUPS
% OF EIGHT DATA POINTS CONTAINED IN EIGHT CONSECUTIVE PES. IN EACH
% SUCCEEDING STEP TWICE THE PREVIOUS NUMBER OF DATA POINTS ARE USED
% IN EACH ITERATION. THIS PROCESS CONTINUES UNTIL THE ENTIRE DATA
% SET IS USED FOR THE FINAL STEP. THE BEGINING OF EACH SECTION OF
% CODE IS LABELED BY THE NUMBER OF DATA POINTS IN A GROUP USED IN
% THAT SECTION (ONE STEP OF THE TRANSFORM). WHEN THE STEP HAS BEEN
% COMPLETED WHICH CORRESPONDS TO THE SIZE OF THE DATA SET THE TRANS-
% FORM IS DONE AND THE REMAIN CODE IS SKIPPED. IT IS IMPORTANT THAT
% EACH STEP IS COMPLETELY INDEPENT OF THE SIZE OR THE NUMBER OF THE
% DATA SET(S) BEING TRANSFORMED.
%
%
FILL          16)          %ALIGN TO 8 PE BOUNDRY.
FFT1(ENTRY)!!
  SETE        E.0R.-E;      %ENABLE ALL PES.
  SETE1       E.0R.-E;      %
  CHWS        32;          %CHANGE TO 32-BIT MODE.
  CACRB       211:8;       %TURN OFF UNDERFLOW.
  LDL(SIZE)   $D1;         %FETCH SIZE INFO.
  SLIT(COUNT) =0;         %RESET COUNT.
  CADD(COUNT) %SIZE;       %SET TO STARTING POINTER.
F8: LDA       0(COUNT);     %LOAD DATA.
  LIT(MODE)   =125252525252525252525252:8)
  RTL        $A,1;        %ROUTE REGISTER A RIGHT.
  COMPC(MODE) ;          %COMPLIMENT MODE PATTERN.
  LDEE1      %MODE;      %ENABLE PES.
  LDS        $R;         %LOAD REGISTER S IN DESIRED PES.
  RTL        $A,-1;      %ROUTE REGISTER A LEFT.
  CHSA       ;          %CHANGE SIGN OF A IN DESIRED PES
  COMPC(MODE) ;          %COMPLIMENT MODE PATTERN.
  LDEE1      %MODE;      %ENABLE PES.
  LDS        $A;         %LOAD REGISTER S IN DESIRED PES.
  LDA        $R;         %LOAD REGISTER A IN SAME PES.
  SETE       E.0R.-E;    %ENABLE ALL PES.
  SETE1      E.0R.-E;    %
  ADD        %S;         %PARTIAL RESULT.
  LIT(MODE)   =14631463146314631463146314:8)
  RTL        $A,2;      %ROUTE REGISTER A RIGHT.
  COMPC(MODE) ;          %COMPLIMENT MODE PATTERN.
  LDEE1      %MODE;      %ENABLE PES.
  LDS        $R;         %LOAD REGISTER S IN DESIRED PES.
  RTL        $A,-2;     %ROUTE REGISTER A LEFT.
  CHSA       ;          %CHANGE SIGN OF A IN DESIRED PES
  COMPC(MODE) ;          %COMPLIMENT MODE PATTERN.
  LDEE1      %MODE;      %ENABLE PES.
  LDS        $A;         %LOAD REGISTER S IN DESIRED PES.
  LDA        $R;         %LOAD REGISTER A IN SAME PES.
  LIT(MODE)   =525252525252525252525252:8)
  LDEE1      %MODE;      %
  SWAPA      ;          % FAKE
  SETE1      E.AN).-E;    % SINES
  CHSA       ;          % AND
  SETE       E.0R.-E;    % COSINES.
  SETE1      E.0R.-E;    %
  ADD        %S;         %PARTIAL RESULT.
  LIT(MODE)   =1703607417036074170360:8)
  TRANSFER   4;          %ROUTING DEFINE.
  MLRN       SIN;        %MULTIPLY BY CONSTANT.
  LIT(MODE)   =525252525252525252525252:8)

```


	SETE	E.OR.=E;	%ENABLE ALL PES.
	SETE1	E.OR.=E;	%
	STS	0(COUNT);	%SAVE OTHER OPERAND.
	LDS	%A;	%SAVE FIRSTUAPERAND.
	LDA	SIN+3;	%FETCH CONSTANTS (W S).
	COMPLEXMULT	SIN+3;	%DEFINE (RGS+SIN).
	ADN	0(COUNT);	%ADD OTHER OPERAND.
	STA	0(COUNT);	%END OF ITERATION.
	TXLTM(COUNT)	,F64;	%ITERATE ONCE PER ROW.
	CTSBF(SIZE)	33,NE64;	%GO ON IF NOT DONE.
	LIT(2)	PAIR(32.0*32.0);	%SET UP EXPONENT FOR INVERSE.
	JUMP	OUT;	%QUIT IF SIZE=64.
NE64:	SLIT(COUNT)	=0;	%ZERO COUNT.
	CADD(COUNT)	%SIZE;	%START WITH FIRST ROW OF DATA.
F128:	LDS	SIN+4;	%FETCH CONSTANTS (W S).
	MULTADDSUB	1;	%DEFINE (DATA+1)*SIN +AND= DATA
	ALIT(COUNT)	=1;	%GO BY PAIRS OF ROWS.
	TXLTM(COUNT)	,F128;	%ITERATE ONCE PEP PAIR OF ROWS.
	CTSBF(SIZE)	32,NE128;	%GO ON IF NOT DONE.
	LIT(2)	PAIR(64.0*64.0);	%SET UP EXPONENT FOR INVERSE.
	JUMP	OUT;	%QUIT IF SIZE=128.
NE128:	SLIT(COUNT)	=0;	%ZERO COUNT.
	CADD(COUNT)	%SIZE;	%START WITH FIRST ROW OF DATA.
P256:	RESETEFF	;	%DEFINE (TFFF=0) LOOP CONTROL.
	LDS	SIN+5;	%FETCH CONSTANTS (W S).
F256:	MULTADDSUB	2;	%DEFINE (DATA+2)*SIN +AND= DATA.
	ALIT(COUNT)	=1;	%INCREMENT COUNT.
	SKIPT	,SKIP1;	%CHECK INNER LOOP.
	CHANGEQUAD	SIN+5;	%DEFINE=MODIFY CONSTANTS.
	SETEFF	;	%DEFINE (TFFF=1) LEAVE NEXT TIME
	SKIP	,F256;	%INNER LOOP.
SKIP1:	ALIT(COUNT)	=1;	%INCREMENT COUNT.
	TXLTM(COUNT)	,P256;	%ITERATE ONCE FOR EACH 4 ROWS.
	CTSBF(SIZE)	31,NE256;	%GO ON IF NOT DONE.
	LIT(2)	PAIR(128.0*128.0);	%SET UP EXPONENT FOR INVERSE.
	JUMP	OUT;	%QUIT IF SIZE=256.
NE256:	SLIT(COUNT)	=0;	%ZERO COUNT.
	CADD(COUNT)	%SIZE;	%START WITH FIRST ROW OF DATA.
	LIT(LOOP)	1,1,0;	%LOOP COUNTER.
P512:	RESETEFF	;	%DEFINE (TFFF=0) LOOP CONTROL.
	LDS	6+SIN(LOOP);	%FETCH CONSTANTS (W S).
F512:	MULTADDSUB	4;	%DEFINE (DATA+4)*SIN +AND= DATA.
	ALIT(COUNT)	=2;	%INCREMENT COUNT.
	SKIPT	,SKIP2;	%CHECK INNER LOOP.
	CHANGEQUAD	6+SIN(LOOP);	%DEFINE=MODIFY CONSTANTS.
	SETEFF	;	%DEFINE (TFFF=1) LEAVE NEXT TIME
	SKIP	,F512;	%INNER LOOP.
SKIP2:	ALIT(COUNT)	=-3;	%INCREMENT COUNT.
	TXLTM(LOOP)	,P512;	%GO BACK FOR NEXT PAIR OF ROWS.
	ALIT(LOOP)	=2;	%RESET LOOP COUNTER.
	ALIT(COUNT)	=5;	%SET COUNT FOR NEXT DATA SET.
	TXLTM(COUNT)	,P512;	%GO BACK FOR NEXT DATA SET.
	CTSBF(SIZE)	30,NE512;	%GO ON IF NOT DONE.
	LIT(2)	PAIR(256.0,256.0);	%SET UP EXPONENT FOR INVERSE.
	JUMP	OUT;	%QUIT IF SIZE=512.
NE512:	SLIT(COUNT)	=0;	%ZERO COUNT.
	CADD(COUNT)	%SIZE;	%START WITH FIRST ROW OF DATA.
	LIT(LOOP)	1,3,0;	%LOOP COUNTER.
P1024:	RESETEFF	;	%DEFINE (TFFF=0) LOOP CONTROL.
	LDS	8+SIN(LOOP);	%FETCH CONSTANTS (W S).
F1024:	MULTADDSUB	8;	%DEFINE (DATA+8)*SIN +AND= DATA.

```

ALIT(COUNT)      =4;
SKIPT            ,SKIP3;
CHANGEQUAD      8+SIN(LOOP);
SETFF           ;
SKIP            ,F1024;
SKIP3:          ALIT(COUNT)      =-7;
                TXLTM(LOOP)     ,P1024;
                ALIT(LOOP)      =-4;
                ALIT(COUNT)      =11;
                TXLTM(COUNT)     ,P1024;
                CTSBF(SIZE)     29,NE1024;
                LIT(2)          PAIR(512.0,512.0);
                JUMP            OUT;
NE1024:         SLIT(COUNT)      =0;
                CADD(COUNT)     $SIZE;
                LIT(LOOP)      1,7,0;
P2048:         RESETFF           ;
                LDS            12+SIN(LOOP);
F2048:         MULTADDSUR       16;
                ALIT(COUNT)     =8;
                SKIPT          ,SKIP4;
                CHANGEQUAD     12+SIN(LOOP);
                SETFF          ;
                SKIP           ,F2048;
SKIP4:         ALIT(COUNT)      =-15;
                TXLTM(LOOP)     ,P2048;
                ALIT(LOOP)      =-8;
                ALIT(COUNT)     =23;
                TXLTM(COUNT)     ,P2048;
                CTSBF(SIZE)     28,NE2048;
                LIT(2)          PAIR(1024.0,1024.0);
                JUMP            OUT;
NE2048:        SLIT(COUNT)      =0;
                CADD(COUNT)     $SIZE;
                LIT(LOOP)      1,15,0;
P4096:         RESETFF           ;
                LDS            20+SIN(LOOP);
F4096:         MULTADDSUR       32;
                ALIT(3)         =16;
                SKIPT          ,SKIP5;
                CHANGEQUAD     20+SIN(LOOP);
                SETFF          ;
                SKIP           ,F4096;
SKIP5:         ALIT(COUNT)      =-31;
                TXLTM(LOOP)     ,P4096;
                ALIT(LOOP)      =-16;
                ALIT(COUNT)     =47;
                TXLTM(COUNT)     ,P4096;
                LIT(2)          PAIR(2048.0,2048.0);
OUT:           CTSBF(SIZE)     15,RETURN;

```

```

XINCREMENT COUNT.
XCHECK INNER LOOP.
XDEFINE-MODIFY CONSTANTS.
XDEFINE (TFFF=1) LEAVE NEXT TIME
XINNER LOOP.
XINCREMENT COUNT.
XGO BACK FOR NEXT PAIR OF ROWS.
XRESET LOOP COUNTER.
XSET COUNT FOR NEXT DATA SET.
XGO BACK FOR NEXT DATA SET.
XGO ON IF NOT DONE.
XSET UP EXPONENT FOR INVERSE.
XQUIT IF DONE (SIZE=1024).
XZERO COUNT.
XSTART WITH FIRST ROW OF DATA.
XLOOP COUNTER.
XINNER LOOP CONTROL. (TFFF=0).
XFETCH CONSTANTS (W S).
XDEFINE (DATA+16)*SIN +AND= DATA
XINCREMENT COUNT.
XCHECK INNER LOOP.
XDEFINE-MODIFY CONSTANTS.
XDEFINE (TFFF=1) LEAVE NEXT TIME
XINNER LOOP.
XINCREMENT COUNT.
XGO BACK FOR NEXT PAIR OF ROWS.
XRESET LOOP COUNTER.
XSET COUNT FOR NEXT DATA SET.
XGO BACK FOR NEXT DATA SET.
XGO ON IF NOT DONE.
XSET UP EXPONENT.
XQUIT IF DONE (SIZE=2048).
XZERO COUNT.
XSTART WITH FIRST ROW OF DATA.
XLOOP COUNTER.
XDEFINE (TFFF=0) LOOP CONTROL.
XFETCH CONSTANTS (W S).
XDEFINE (DATA+32)*SIN +AND= DATA
XINCREMENT COUNT.
XCHECK INNER LOOP.
XDEFINE-MODIFY CONSTANTS.
XDEFINE (TFFF=1) LEAVE NEXT TIME
XINNER LOOP.
XINCREMENT COUNT.
XGO BACK FOR NEXT PAIR OF ROWS.
XRESET LOOP COUNTER.
XSET COUNT FOR NEXT DATA SET.
XGO BACK FOR NEXT DATA SET.
XSET UP EXPONENT.
XLEAVE IF NOT INVERSE FFT.

```

```

%
% THE FOLLOWING CODE IS EXECUTED IF AN INVERSE TRANSFORM IS
% REQUESTED. THE ONLY OPERATION REQUIRED IS THAT EACH ELEMENT OF
% THE SOLUTION MUST BE DIVIDED BY N(SIZE), THE NUMBER OF DATA POINTS
% IN THE FUNCTION. SINCE N IS A POWER OF TWO THIS CAN BE DONE BY
% SUBTRACTING LOG(BASE 2)N FROM EACH EXPONENT. THE PROPER EXPONENTS
% ARE CREATED AT A TIME WHEN N(SIZE) IS KNOWN EXACTLY AND THEY ARE
% PUT IN ACAR2(LOOP) JUST BEFORE EXECUTION IS TRANSFERRED TO OUT.
%

```

```

SLIT(COUNT)      =0;
CADD(COUNT)     $SIZE;
XZERO COUNT.
XSTART WITH FIRST ROW OF DATA.

```

INVERSE:	LDA	O(COUNT);	%FETCH DATA.
	SBEX	\$C2;	%DIVIDE BY SIZE (NO UNDERFLOW).
	TXLTM(COUNT)	, INVERSE;	%DO FOR ALL ROWS OF DATA.
RETURN:	LDL(3)	\$D2;	%FETCH RETURN ADDRESS.
	LDL(0)	\$D3;	%RESTORE ACAR0.
	LDL(1)	\$D4;	%RESTORE ACAR1.
	EXCHL(3)	\$ICR;	%RETURN TO CALLING PROGRAM.
	END	FFT1.	

S RESLT LIST

BEgin

```

                                00000090
                                00000100
                                00000110
                                00000200
                                00000300
                                00000400
                                00000500
                                00000600
                                00000700
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                                00004300
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13. ABSTRACT This report is a description of a Fast Fourier Transform (FFT) subroutine written in assembly language for the ILLIAC IV computer. The subroutine uses the Cooley-Tukey algorithm for performing discrete Fourier transforms. The parallel nature of the Cooley-Tukey method lends itself very well to a highly parallel machine like ILLIAC IV. Timing simulation results have shown that this program will perform Fourier transforms faster than they are being done on any existing computer system.			

14. KEY WORDS	LINK A		LINK B		LINK C	
	ROLE	WT	ROLE	WT	ROLE	WT
Seismic Data Processing ASK Fast Fourier Transform (FFT) Discrete Fourier Transform (DFT) Time Series Frequency Spectrum						





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